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J.C. S.
PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10017734	FILING DATE 12/18/2001	CLASS 257	SUBCLASS 387	GAU 2811	EXAMINER <i>Tscha, Tan</i>
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** FOREIGN APPLICATIONS VERIFIED:

CHINA 00135808.1 12/21/2000

PG-PUB <input type="checkbox"/>	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
Verified and Acknowledged Examiner's initials		ATTORNEY DOCKET NO B784.312-1
TITLE : Power semiconductor switching devices with low power loss and method for fabricating the same		
U.S. DEPT. OF COMM /PAT. & TM PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Fig.-Drwg.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE Application Examiner		
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